

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,360,024 B2  
APPLICATION NO. : 10/687460  
DATED : April 15, 2008  
INVENTOR(S) : T. Hironaka et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

COLUMN

LINE

20 65  
(Claim 3, line 2)

“multiport” should read --multi-port--

20 66  
(Claim 3, line 3)

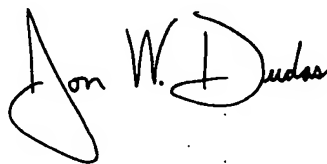
“multi” should read --multi- --

21 5  
(Claim 5, line 1)

“integrated” should read --integrated--

Signed and Sealed this

Eleventh Day of November, 2008



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*